

SPECIFICATION

Revision: 0.0

This module uses ROHS material

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REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2022-04-08	First release	Preliminary

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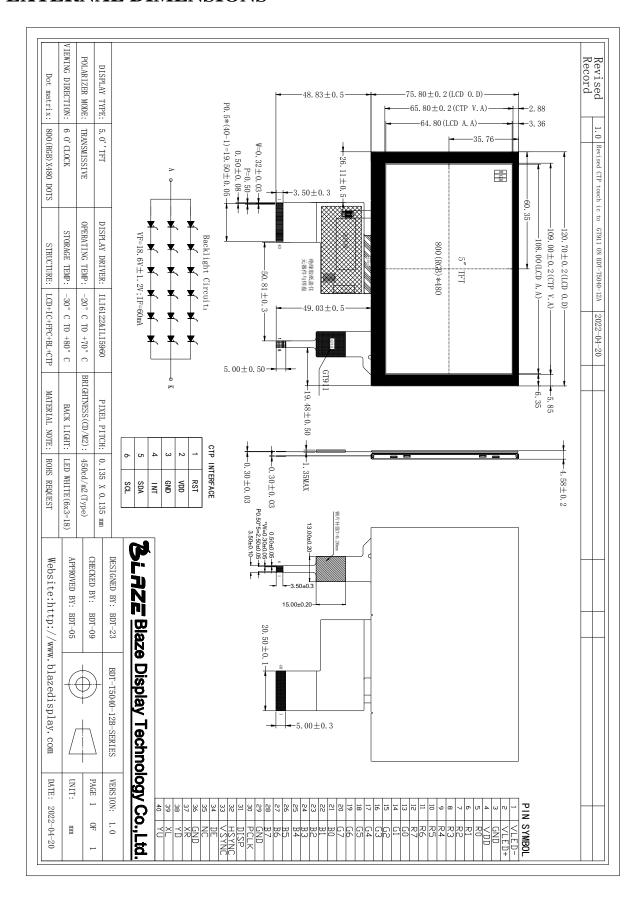


1. GENERAL INFORMATION

Item of general information	Contents	Unit
LCD Display Size (Diagonal)	5.0	inch
LCD Display Type	TFT/TRANSMISSIVE	-
LCD Display Mode	Normally White	-
Recommended Viewing Direction	12	o'clock
Gray inversion Direction	6	o'clock
LCM Module size (W×H×T)	120.70×75.80×4.60	mm
Active area (W×H)	108.00×64.80	mm
Number of pixels (Resolution)	800(RGB)×480	pixel
Pixel pitch (W×H)	0.135×0.135	mm
Color Pixel Arrangement	RGB Stripe	-
LCD Driver IC	-	-
Interface Type	Parallel 24bit RGB interface	-
Color Numbers	16.7M	-
Backlight Type	White LED	-



2, EXTERNAL DIMENSIONS



3. ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
LCD supply voltage	VDD	-0.5	5.0	V
Operating temperature	Тор	-20	70	$^{\circ}$
Storage temperature	Tst	-30	80	$^{\circ}$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings mean the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4. ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC	Symbol	Min.	Тур.	Max.	Unit	
characteristics	v		. 1			
LCD operating voltage	VDD	2.3	3.3	3.6	V	
VDD Input Current	Idd	-	-	-	mA	
Input voltage 'H' level	VIH	0.7*VDD	-	VDD	V	
Input voltage 'L' level	VIL	VSS	-	0.3*VDD	V	
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V	
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V	

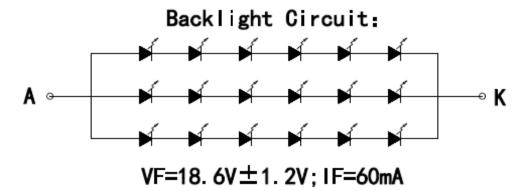


5, BACKLIGHT CHARACTERISTICS

Item of backlight characteristics	Symbol	Min.	Тур.	Max.	Unit	Remark
Forward Voltage	Vf	17.4	18.6	19.8	V	Note1
Forward Current	If	-	60	-	mA	-
Number of LED	-	-	6*3=18	-	Piece	-
LED Connection mode	P/S	-	Serial/Parallel	-	-	-
Lifetime of LED	-	-	20000	-	hour	Note2

Note:

- Note1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and If=60mA.
- Note2: The LED lifetime define as the estimated time to 50% degradation of initial luminous. The LED lifetime could be decreased if operating If is larger than 60mA.
- ➤ Backlight circuit:





6, CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	800×480	pixel	-
Surface Hardness	6Н	-	-
Transparency	≥82%	-	-
Driver IC	GT911	-	-
Interface Type	I2C	-	-
Support Points	5	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-



7, ELECTRO-OPTICAL CHARACTERISTICS

Item (electro-op character	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0-0	-	25	50	ms	FIG 1.	4
Contrast F	Ratio	CR	θ=0 Ø=0	-	500	-	-	FIG 2.	1
Luminance un	iformity	δWHITE	Ta=25°C	-	80	-	%	FIG 2.	3
Surface Lum	ninance	Lv	14 25 5	-	450	-	cd/m2	FIG 2.	2
CIE (x, y)	White	White x	θ=0 ∅=0	0.26	0.31	0.36		FIG 2.	5
chromaticity	Willie	White y	7a=25°C	0.28	0.33	0.38	_	110 2.	3
	Ø=90(1	2 o'clock)		-	50	-	deg		
Viewing	Ø=270(6 o'clock)	CR ≥ 10	-	70	_	deg	FIG 3.	6
angle range	Ø=0(3 d	o'clock)	CK 2 10	-	70	-	deg	F1G 3.	O
	Ø=180(9 o'clock)		-	70	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (δ WHITE) is determined by measuring luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.



 $\delta \text{WHITE} = \frac{\textit{Minimum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{\textit{Maximum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

Note 5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

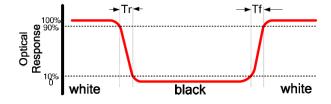


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance

uniformity, CIE (x, y) chromaticity

A:H/6; B:V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

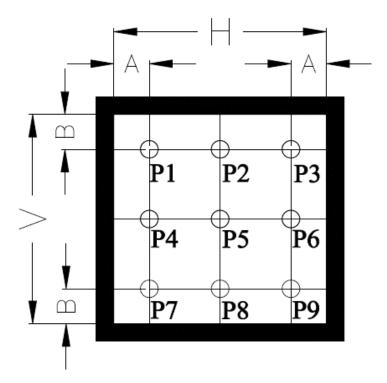
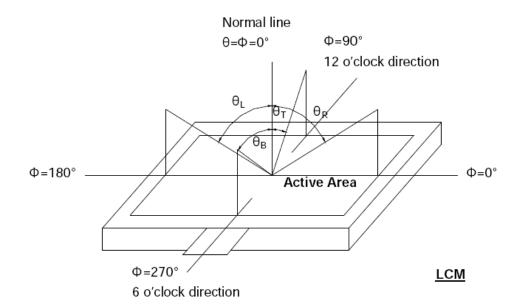


FIG.3. The definition of viewing angle



8. INTERFACE DESCRIPTION

A. LCD Interface Description

NO.	Symbol	I/O	DESCRIPTION
1	LED-K	Power supply	Backlight Cathode
2	LED-A	Power supply	Backlight Anode
3	GND	Power supply	Power ground
4	VDD	Power supply	Digital Power supply(3.3V Typ.)
5~12	R0~R7	I	8bit digital Red data input(R0:LSB; R7:MSB)
13~20	G0~G7	I	8bit digital Green data input(G0:LSB; G7:MSB)
21~28	B0~B7	I	8bit digital Blue data input(B0:LSB; B7:MSB)
29	GND	Power supply	Power ground
30	DCLK	I	Clock signal. Latching data at the rising edge.
31	DISP	I	Standby setting pin, it should be connected to VDD in normal operation mode. If connected to GND, the driver IC is in standby mode.
32	HSYNC	I	Horizontal Sync input. Negative polarity.
33	VSYNC	I	Vertical Sync input. Negative polarity.
34	DEN	I	Data input Enable. Active high to enable the data input Bus.
35	NC	-	No Connection
36	GND	Power supply	Power ground
37	XR	-	RTP pin
38	YD	-	RTP pin
39	XL	-	RTP pin
40	YU	-	RTP pin

B、CTP Interface Description

NO.	Symbol	I/O	DESCRIPTION
1	RESET	I	CTP external reset signal, Low is active
2	VDD	Power supply	CTP Power input
3	GND	Power supply	Power ground
4	INT	О	CTP External interrupt to the host
5	SDA	I/O	CTP I2C data input and output
6	SCL	I	CTP I2C clock input



9、INPUT TIMING

Parallel RGB input Timing table

Parameter	Symbol		Unit		
rarameter	Symbol	Min.	Тур.	Max.	Omt
DCLK frequency@ Frame rate=60Hz	DCLK	-	30	50	MHz
Horizontal display area	thd		800		DCLK
1 Horizontal Line	th	-	928	-	DCLK
HSYNC pulse width	thpw	1	48	-	DCLK
HSYNC Back Porch(Blanking)	thb	-	88	-	DCLK
HSYNC Front Porch	thfp	-	40	-	DCLK
Vertical display area	tvd		480		Н
VSYNC period time	tv	-	525	-	Н
VSYNC pulse width	tvpw	-	3	-	Н
VSYNC Back Porch(Blanking)	tvb	-	32	-	Н
VSYNC Front Porch	tvfp	-	13	-	Н

10, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- ➤ Air bubble in the LCD;
- Sealleak;
- Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B, Remark:

- ➤ The test samples should be applied to only one test item.
- ➤ Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



11、INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

11.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

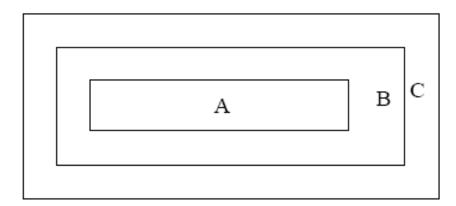
Minor defect: AQL 1.5

11.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25°C and normal humidity $60\pm15\%$ RH)

11.3 Definition of Inspection Item.

A, Definition of inspection zone in LCD.





Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B, Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the				
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under				
	black pattern.				
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying				
	under pure red, green, blue picture, or pure whiter picture.				

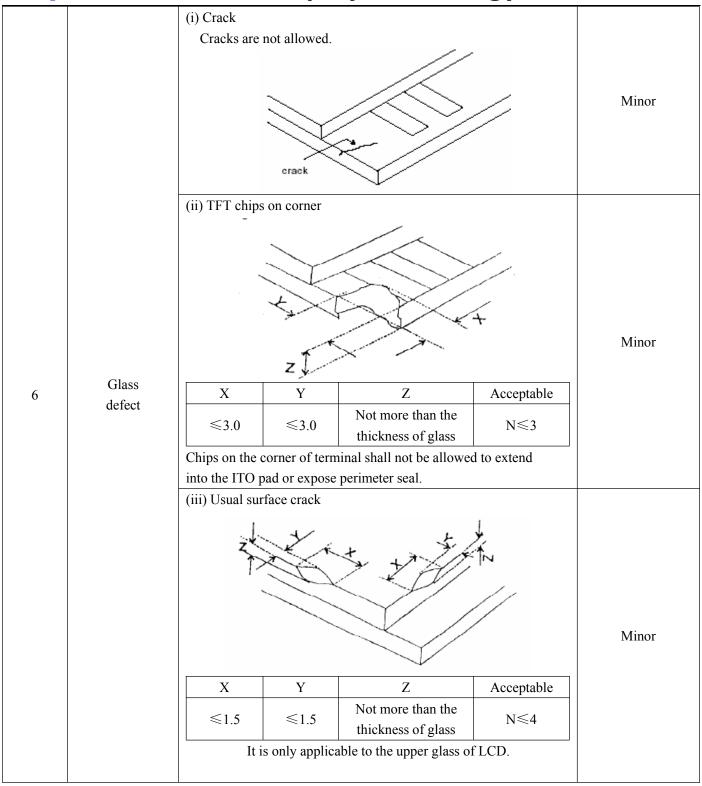
11.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

5. Minor Defect

Item No.	Items to be	Inspection standard						Classification of defects					
				Acceptable Qty A+B									
		Bright pixel dot				7"	$\frac{7110}{7 \sim 10.1"}$, >	>10.1"	С			
				t	1		2		3				
	Bright dot	l 	ark pixel dot		4		4		4	Ac			
1	/dark dot	2brigh	nt dots adja	cent	0		0		0	Acceptable	Minor		
	defect	2dark	dots adjac	cent	0		0		0	able			
		Total bri	ght and da	rk dots	5		6		7				
		Note: Mir	nimum dist	ance bet	ween	defect	tive dots	is m	ore than	5mm;			
			' function			_			-				
		material a	nd other re	easons ar	e judg				ct of 5.2.				
	Dot defect		Zone		Acceptable Qty								
							<u>r+B</u>						
		Size(mm		4.3"~			10.1"		>10.1"	С			
		Ф≤0	Accep	table	able Acceptable	Acc	ceptable	Ac					
2		() ↓ y	0.2<	Ф≤0.5	4			5		6	Acceptable	Minor	
		Φ >	>0.5	0			0		0				
			um distance										
	Linear defect		Zone			Acc	ceptable	Qty					
3		Size (mm)			A+B								
		Length	Width	4.3"~	~7"	7~	10.1"	>	>10.1"	С			
		Ignore	W≤0.05	Accept	table	Acce	eptable	Acc	ceptable	Ac	Minor		
				L≤5.0	0.05 < W≤0.1	4			5		6	Acceptable	
		L>5.0	W>0.1	0			0		0	je			

				piay .				,	
		5.4.1 Pola	rizer Posit	ion					
		(i) Shifting	ng in positi	on should not	exceed the gla	ass outline			
		dimension.							
		(ii) Incor	mplete cov	ering of the vi	ewing area du	e to shifting is	s not		
		allowed.							
		5.4.2 Dirt	5.4.2 Dirt on polarizer						
		Dirt which can be wiped easily should be acceptable.							
		5.4.3 Pola	rizer Dent	& Air bubble					
		Zone Acceptable Qty							
		A+B							
		Size(mm	1)	4.3"~7"	7~10.1"	>10.1"	С		
		Φ \$	≤0.2	Acceptable	Acceptable	Acceptable	Acı		
		0.2<	Φ≤0.5	4	5	6	Acceptable		
4	Polarizer	Φ>	>0.5	0	0	0	ole	Minor	
	defect	5.4.4 Pol	arizer scra	atch	<u> </u>	<u> </u>	1		
				scratch can b	e seen after o	over assemb	ling		
		1	•				-		
		or in the operating condition, judge by the linear defect of 5.3. (ii) If the polarizer scratch can be seen only in non-operating							
		` ′	condition or some special angle, judge by the following:						
			Zone	Acceptable Qty					
		Size (mm)		A+B					
					4 2" - 7" 7 - 10 1" > 10 1"				
		Length	Width	4.3"~7"	7~10.1"	>10.1"	С		
		Ignore	W≤0.05	Acceptable	Acceptable	Acceptable	Ac		
		1.0 <l< td=""><td>0.05<</td><td>4</td><td>5</td><td>6</td><td rowspan="2">Acceptable</td></l<>	0.05<	4	5	6	Acceptable		
		≤5.0	W≤0.20						
		L>5.0	W>0.2	0	0	0			
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.							
	White/Black dot (MURA)			r: ND3%; I 0.5mm, N≦4;		-		Minor	





Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
		No soldering missing	Major
3	Soldering defects	No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil (Φ 0.5 mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ0.2mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
11	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor
	3. Chips	(3/2) H ≥h ≥(1/2) H \$\int_{h} \hat{\text{H}}\$	Minor
	4. Solder ball/Solder	a. The spacing between solder ball and the conductor or solder pad h \geq 0.13 mm. The diameter of solder ball d \leq 0.15 mm.	Minor
	splash	b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor



	c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major