Pi – Tek

OLED Module

SPECIFICATIONS

MODEL NO. : PG12864KW
PRODUCT TYPE: STANDARD

This specification may be changed without any notices in order improve performance or quality etc.

Content

History of versions and modifications

Revision	Date	Description	Changed By
1.0		Preliminary specification	

Coding system

P G 12864 K W

P: PI-TEK INC.

G: Graphic

12864: 128 x 64

K: Model

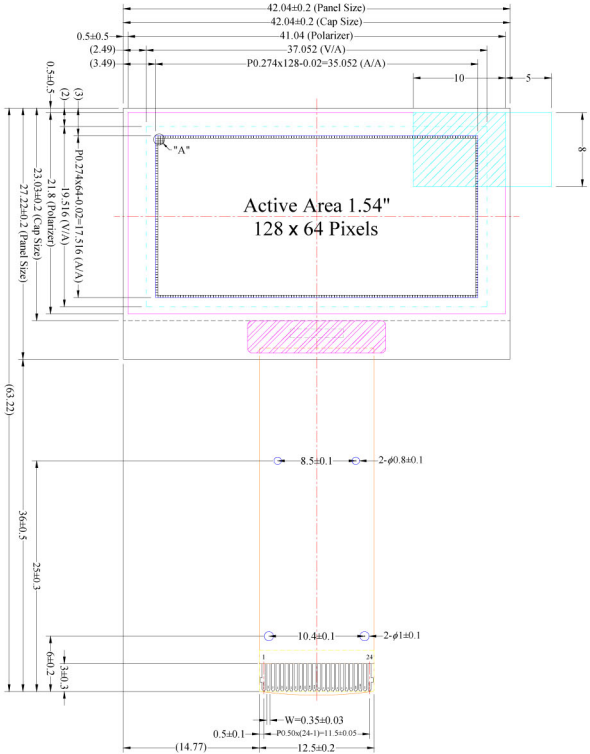
W: White (Color)

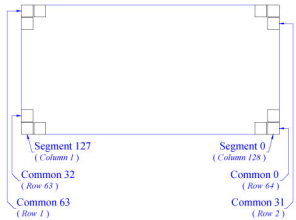
Functions and Features

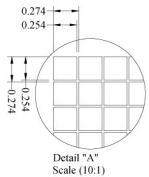
- 128X64 Graphic
- Built-in controller
- viewing angle Free
- Wide Temperature -40℃ ~ +80℃ (Operating)
- RoHS compliant

Mechanical Specification

Item	Description	
Product No.	PG12864KW	
Inch	1.54”	
Color	White	
Active Area	35.052(W)×17.516(H)	mm
Panel Size	42.04(W)×27.22(H)×1.45(D)	mm
Dot Size	0.254(W)×0.254(H)	mm
Dot Pitch	0.274(W)×0.274(H)	mm
Display Format	128× 64	
Duty Ratio	1/64	Duty
Controller	SSD1309 or Equivalent	
Operation Temperature	-40~80	°C
Storage Temperature	-40~85	°C
Response Time	≤10	us
Assembly	Connector	







Pin	Symbol
1	N.C. (GND)
2	VLSS
3	VSS
4	N.C.
5	VDD
6	BS1
7	BS2
8	CS#
9	RES#
10	D/C#
11	R/W#
12	E/RD#
13	D0
14	D1
15	D2
16	D3
17	D4
18	D5
19	D6
20	D7
21	IREF
22	VCOMH
23	VCC
24	N.C. (GND)

Notes:

1. Color: White
2. Driver IC: SSD1309
3. FPC Number: UT-0205-P05
4. Interface:
   8-bit 68XX/80XX Parallel, 4-wire SPI, I2C
5. General Tolerance: ±0.30
6. The total thickness (1.55 Max) is without polarizer protective film & remove tape.
   The actual assembled total thickness with above materials should be 1.80 Max.

Pin Description

Power Supply

Pin Number	Symbol	Type	Function
5	VDD	P	**Power Supply for Logic Circuit** This is a voltage supply pin. It must be connected to external source.
3	VSS		**Ground of OEL System** This is a ground pin. It also acts as a reference for the logic pins. It must be connected to external ground.
23	VCC		**Power Supply for OEL Panel** This is the most positive voltage supply pin of the chip. It must be supplied externally.
2	VLSS		**Ground of Analog Circuit** This is an analog ground pin. It should be connected to VSS externally.

Driver

Pin Number	**Symbol**	**Type**	**Function**
21	IREF	I	**Current reference for Brightness Adjustment** This pin is segment current reference pin. A resistor should be connected between this pin and VSS. Set the current at 10μA maximum.
22	VCOMH	O	**Voltage Output High Level for COM Signal** This pin is the input pin for the voltage output high level for COM signals. A tantalum capacitor should be connected between this pin and VSS.

Interface

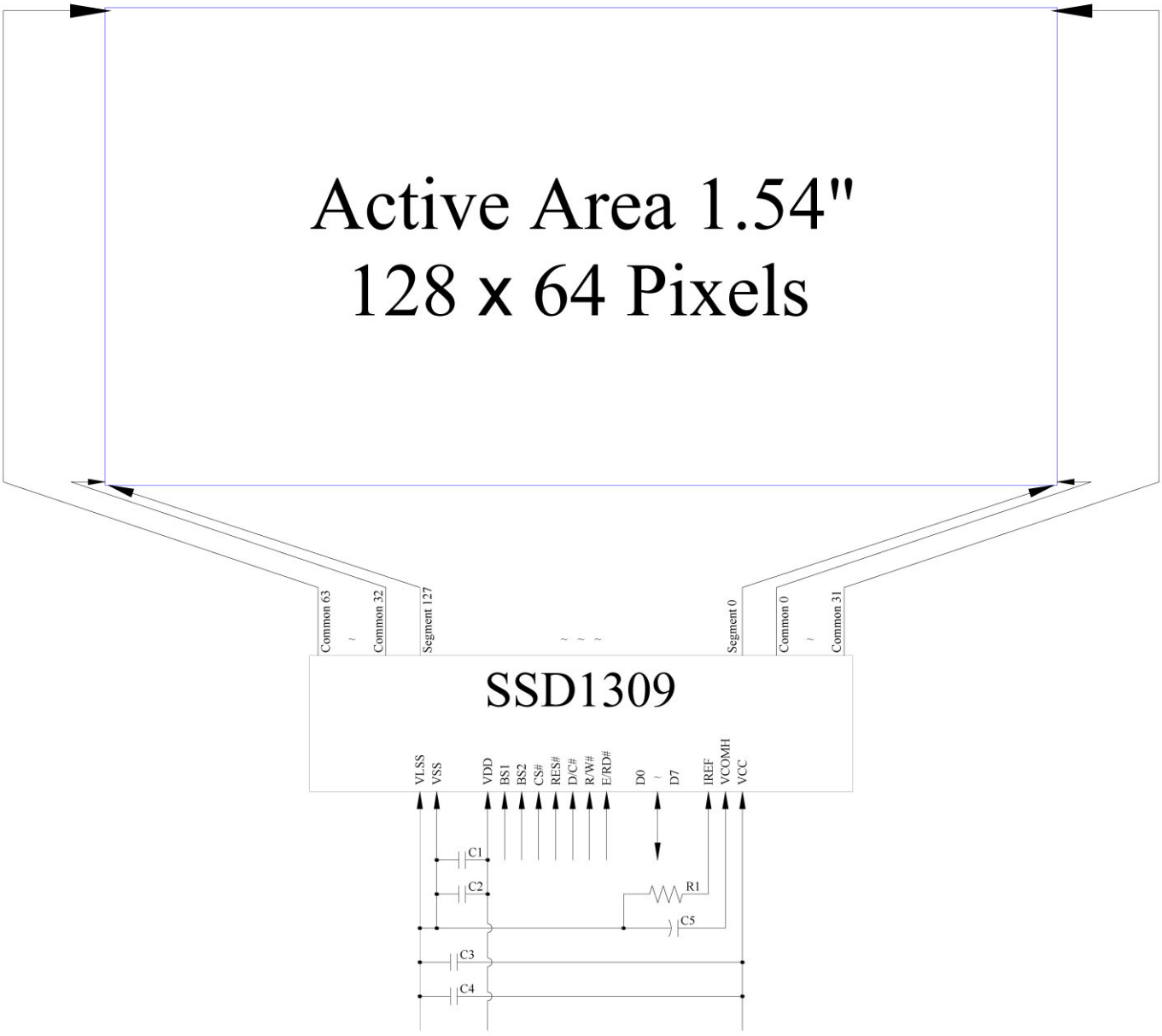
<table>
<tr><th>Pin Number</th><th>Symbol</th><th>Type</th><th>Function</th></tr>
<tr><td>6
7</td><td>BS1
BS2</td><td rowspan="7">I</td><td>Communicating Protocol Select
These pins are MCU interface selection input. See the following table:
<table>
<tr><td></td><td>BS1</td><td>BS2</td></tr>
<tr><td>I2C</td><td>1</td><td>0</td></tr>
<tr><td>Serial</td><td>0</td><td>0</td></tr>
<tr><td>68xx Parallel</td><td>0</td><td>1</td></tr>
<tr><td>80xx Parallel</td><td>1</td><td>1</td></tr>
</table></td></tr>
<tr><td>9</td><td>RES#</td><td>Power Reset for Controller and Driver
This pin is reset signal input. When the pin is low, initialization of the chip is executed.</td></tr>
<tr><td>8</td><td>CS#</td><td>Chip Select
This pin is the chip select input. The chip is enabled for MCU communication only when CS# is pulled low.</td></tr>
<tr><td>10</td><td>D/C#</td><td>Data/Command Control
This pin is Data/Command control pin. When the pin is pulled high, the input at D7~D0 is treated as display data. When the pin is pulled low, the input at D7~D0 will be transferred to the command register. For detail relationship to MCU interface signals, please refer to the Timing Characteristics Diagrams. When the pin is pulled high and serial interface mode is selected, the data at SDIN is treated as data. When it is pulled low, the data at SDIN will be transferred to the command register. In I2C mode, this pin acts as SA0 for slave address selection.</td></tr>
<tr><td>12</td><td>E/RD#</td><td>Read/Write Enable or Read
This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low.</td></tr>
<tr><td>11</td><td>R/W#</td><td>Read/Write Select or Write
This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to “High” for read mode and pull it to “Low” for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low.</td></tr>
</table>

13~20	D0~D7	I/O	**Host Data Input/Output Bus** These pins are 8-bit bi-directional data bus to be connected to the microprocessor's data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK. When I2C mode is selected, D2, D1 should be tired together and serve as SDAOUT, SDAIN in application and D0 is the serial clock input, SCL.

Reserve

Pin Number	Symbol	Type	Function
4	N.C.	-	**Reserved Pin** The N.C. pin between function pins is reserved for compatible and flexible design.
1,24	N.C. (GND)	-	**Reserved Pin (Supporting Pin)** The supporting pins can reduce the influences from stresses on the function pins. These pins must be connected to external ground .

Block Diagram



MCU Interface Selection: BS1 and BS2

Pins connected to MCU interface: CS#, RES#, D/C#, R/W#, E/RD#, and D0~D7

C1, C3: 0.1μF

C2: 4.7μF

C4: 10μF

C5: 4.7μF / 25V Tantalum Capacitor

R1: 910kΩ, R1 = (Voltage at IREF - BGGND) / IREF

DC Characteristics

Item	Symbol	Condition	Min.	Type	Max.	Unit
Supply Voltage for Logic	V_{DD}		1.65	2.8	3.3	Volt
Supply Voltage for Display	V_{CC}	Note 5	12.0	12.5	13.0	Volt
Operating Current for VDD	I_{DD}		-	180	300	μA
Operating Current for VCC	I_{CC}	Note 6	-	12.7	15.9	mA
		Note 7	-	20.5	25.6	mA
		Note 8	-	35.8	44.8	mA
Sleep Mode Current for VDD	$I_{DD,SLEEP}$		-	1	5	μA
Sleep Mode Current for VCC	$I_{CC,SLEEP}$		-	2	10	μA

Note 5: Brightness (Lbr) and Supply Voltage for Display (VCC) are subject to the change of the panel characteristics and the customer's request.

Note 6: VDD = 2.8V, VCC = 12.5.0V, 30% Display Area Turn on.

Note 7: VDD = 2.8V, VCC = 12.5.0V, 50% Display Area Turn on.

Note 8: VDD = 2.8V, VCC = 12.5.0V, 100% Display Area Turn on.

Optical Characteristics

Item	Symbol	Conditions	Min.	Typ	Max.	Unit
Brightness(White)	Lbr	Note 5	-	120	-	cd/㎡
C.I.E. (White)	(X)	C.I.E 1931	0.25	0.29	0.33	
	(Y)		0.57	0.31	0.35	
Dark Room Contrast	CR	-	-	>10000:1	-	
Viewing angle range	-	-	-	Free	-	Degree

* Optical measurement taken at VDD = 2.8V, VCC = 12.5V.

Absolute Maximum rating

Item	Symbol	Min.	Typ.	Max.	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	-	4	Volt	1,2
Supply Voltage for Display	VCC	0	-	15	Volt	1,2
Life Time (55 cd/㎡)		---	70,000	---	Hour	3

Note 1: All the above voltages are on the basis of “VSS = 0V”.

Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 3. “Optics ”. If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate.

Note 3: VCC = 12.5V, Ta = 25°C, 50% Checkerboard.

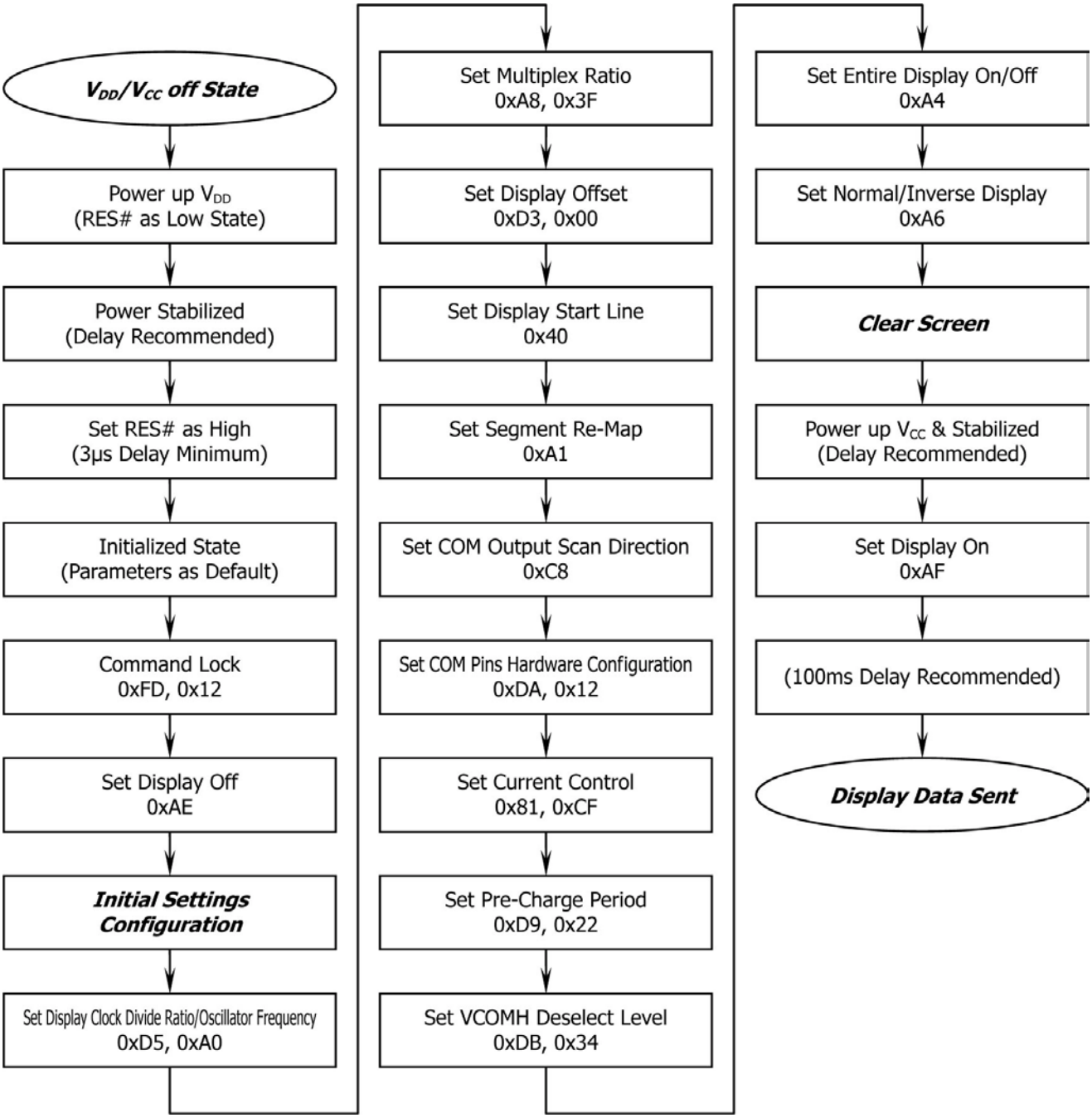
AC Characteristics

please refer “SSD1309” specification.

Actual Application Example

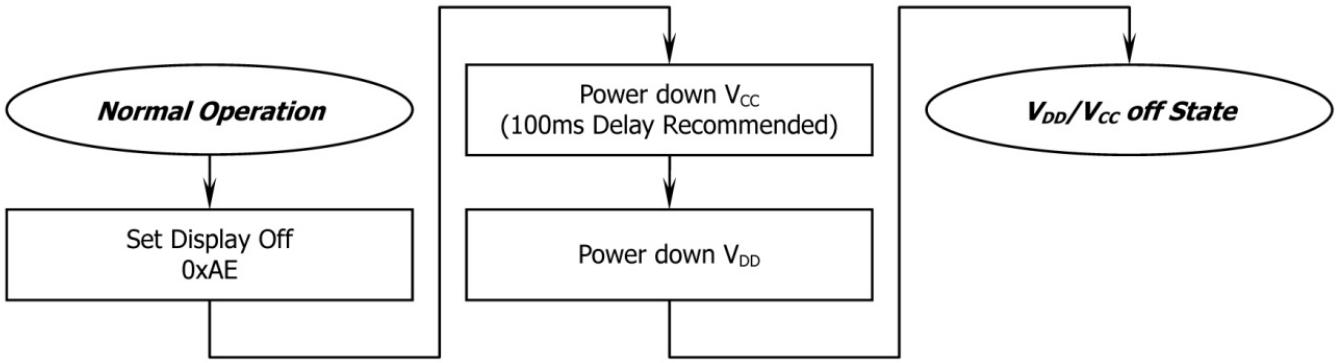
Command usage and explanation of an actual example

<Power up Sequence>

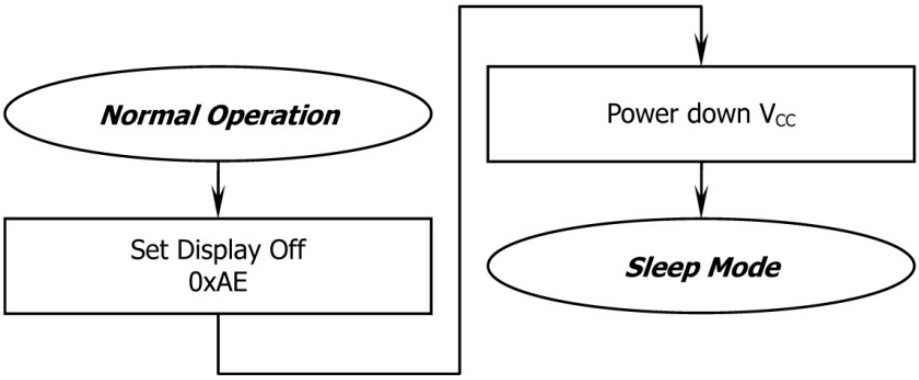


If the noise is accidentally occurred at the displaying window during the operation, please reset the display in order to recover the display function.

<Power down Sequence>



<Entering Sleep Mode>



<Exiting Sleep Mode>